# Not Recommended for New Designs

This product was manufactured for Maxim by an outside wafer foundry using a process that is no longer available. It is not recommended for new designs. The data sheet remains available for existing users.

A Maxim replacement or an industry second-source may be available. Please see the QuickView data sheet for this part or contact technical support for assistance.

For further information, contact Maxim's Applications Tech Support.

# M/IXI/M

### 5V/3.3V/3V 2A Step-Down, PWM, Switch-Mode DC-DC Regulators

#### General Description

The MAX727/MAX728/MAX729 are monolithic, bipolar, pulse-width modulation (PWM), switch-mode, step-down DC-DC regulators. Each is rated at 2A. Very few external components are needed for standard operation because the power switch, oscillator, feedback, and control circuitry are all on-chip. Employing a classic buck topology, these regulators perform high-current step-down functions.

These regulators have excellent dynamic and transient response characteristics, while featuring cycle-by-cycle current limiting to protect against overcurrent faults and shortcircuit output faults. They also have a wide 8V to 40V input range.

Each regulator is available in a 5-pin TO-220 package. These devices have a preset 100kHz oscillator frequency and a preset current limit of 2.6A. See the MAX724/ MAX726 data sheet for more applications information.

#### Applications

Distributed Power from High-Voltage Buses High-Current, High-Voltage Step-Down Applications Multiple-Output Buck Converter

Typical Operating Circuit

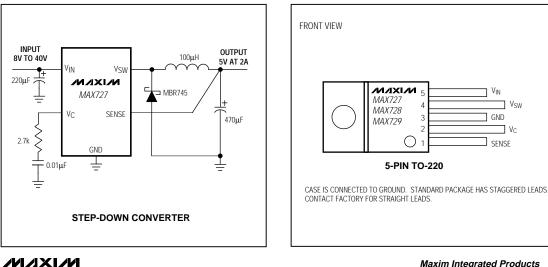
- + Input Range: Up to 40V
- + 2A On-Chip Power Switch
- Fixed Outputs: 5V (MAX727) 3.3V (MAX728) 3V (MAX729)
- 100kHz Switching Frequency
- Excellent Dynamic Characteristics
- Few External Components
- ♦ 8.5mA Quiescent Current
- TO-220 Package

#### Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX727CCK	0°C to +70°C	5 TO-220
MAX727ECK	-40°C to +85°C	5 TO-220
MAX728CCK	0°C to +70°C	5 TO-220
MAX728ECK	-40°C to +85°C	5 TO-220
MAX729CCK	0°C to +70°C	5 TO-220
MAX729ECK	-40°C to +85°C	5 TO-220

Features

### Pin Configuration



### Call toll free 1-800-998-8800 for free samples or literature.

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### ABSOLUTE MAXIMUM RATINGS

Input Voltage	
(Note 1)	
SENSE Pin Voltage	
Operating Temperature Ranges	
MAX72_CCK	
MAX72_ECK40°C to +85°C	

Junction Temperature Ranges:	
MAX72_CCK0°C to +125°C	
MAX72_ECK 40°C to +125°C	
Storage Temperature Range65°C to +160°C	
Lead Temperature (soldering, 10sec)+300°C	

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### **ELECTRICAL CHARACTERISTICS**

( $V_{IN} = 25V$ ,  $T_j = T_{MIN}$  to  $T_{MAX}$ , unless otherwise noted.)

PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS	
Input Supply Voltage Range			8.0		40.0	V	
	I <sub>SW</sub> = 0.5A				1.2	V	
Switch-On Voltage (Note 2)	I <sub>SW</sub> = 2A				1.7		
Switch-Off Leakage	$V_{IN} = 25V, V_{SW} = 0V$	$T_j = +25^{\circ}C$			150		
Switch-Off Leakage	$V_{IN} = 40V$ , $V_{SW} = 0V$	$T_j = +25^{\circ}C$			250	μΑ	
Supply Current (Note 3)	$V_{IN} \le 40V$ , $V_{SENSE} = 5.5V$			8.5	11	mA	
Minimum Supply Voltage	Normal Mode			7.3	8.0		
	Start-Up Mode (Note 4)	T <sub>j</sub> ≥0°C		3.5	4.8	V	
		Tj < 0°C		3.5	5.0		
Switch-Current Limit (Note 5)		•	2.0	2.6	3.2	A	
Maximum Duty Cycle			85	90		%	
		$T_j = +25^{\circ}C$	90	100	110	kHz	
Switching Frequency		Tj ≤ +125°C	85		120		
	V <sub>OUT</sub> = V <sub>SENSE</sub> = 0V (Note 5)	$T_j = +25^{\circ}C$		20			
Switching Frequency Line Regulation $8V \le V_{IN} \le 40V$			0.03	0.10	%/V		
Error-Amplifier Voltage Gain	$1V \le V_C \le 4V$	$T_j = +25^{\circ}C$		2000		V/V	
Error-Amplifier Transconductance		$T_j = +25^{\circ}C$	3000	5000	9000	µmho	
Error-Amplifier Source Current	V <sub>SENSE</sub> = V <sub>OUT</sub> - 10%	$T_j = +25^{\circ}C$	100	140	225	μA	
Error-Amplifier Sink Current	VSENSE = VOUT + 10%	$T_j = +25^{\circ}C$	0.6	1.0	1.7	mA	

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### 5V/3.3V/3V 2A Step-Down, PWM, Switch-Mode DC-DC Regulators

#### **ELECTRICAL CHARACTERISTICS (continued)**

 $(V_{IN} = 25V, T_j = T_{MIN}$  to  $T_{MAX}$ , unless otherwise noted.)

PARAMETER	CONDITIONS		MIN		MAX	UNITS
		MAX727	4.85	5.00	5.15	
SENSE Voltage	$V_{C} = 2V$	MAX728	3.20	3.30	3.40	V
_		MAX729	2.90	3.00	3.10	1
		MAX727	3.0	5.0	8.0	
SENSE Pin Divider Resistance	$T_j = +25^{\circ}C$	MAX728	2.5	4.2	7.0	kΩ
		MAX729	2.2	3.8	6.5	1
Output Voltage Tolerance	V <sub>OUT</sub> (nominal) = 5V (MAX727), 3.3V (MAX728), or 3V (MAX729); all conditions of input voltage, output voltage, and load current	$T_j = +25^{\circ}C$		±0.5	±2.0	- %
		T <sub>j</sub> = T <sub>MIN</sub> to T <sub>MAX</sub>		±1.0	±3.0	
Output Voltage Line Regulation	$8V \le V_{IN} \le 40V$			0.005	0.020	%/V
V <sub>C</sub> Voltage	0% duty cycle	$T_j = +25^{\circ}C$		1.5		V
V <sub>C</sub> Voltage Temperature Coefficient	0% duty cycle	Tj = TMIN to TMAX		-4		mV/°C
Thermal Resistance Junction to Case (Note 6)					4.0	°C/W

Note 1: Do not exceed switch-to-input voltage limitation.

Note 2: For switch currents between 1A and 2A, maximum switch-on voltage can be calculated via linear interpolation.

Note 3: By setting the SENSE pin to 5.5V, the V<sub>C</sub> pin is forced to its low clamp level and the switch duty cycle is forced to zero, approximating the zero load condition.

**Note 4:** For proper regulation, total voltage from  $V_{IN}$  to GND must be  $\ge 8V$  after start-up.

Note 5: To avoid extremely short switch-on times, the switch frequency is internally scaled down when V<sub>SENSE</sub> is less than 2.6V (MAX727), 2.0V (MAX728), or 1.8V (MAX729). Switch current limit is tested with V<sub>SENSE</sub> adjusted to give a 1µs minimum switch-on time.

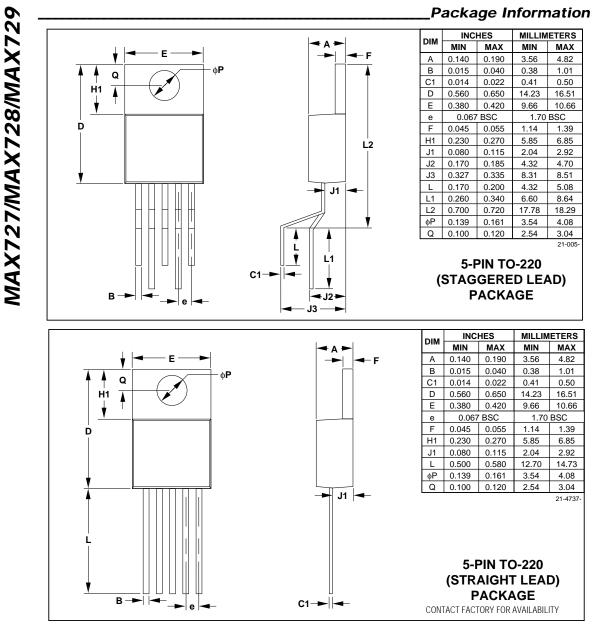
Note 6: Guaranteed, not production tested.

#### \_Pin Description

PIN	NAME	FUNCTION		
1	SENSE	SENSESENSE Input is the internal error amplifier's input, and should be directly connected to VOUT. SENSE also aids current limiting by reducing oscillator frequency when VOUT is low.		
2	V <sub>C</sub>	Error-Amplifier Output. A series RC network connected to this pin compensates the MAX727/MAX728/MAX729. Output swing is limited to about 5.8V in the positive direction and -0.7V in the negative direction. V <sub>C</sub> can also synchronize the MAX727/MAX728/MAX729 to an external TTL clock in the 115kHz to 170kHz range. See MAX724/MAX726 data sheet.		
3	3 GND Ground requires a short, low-noise connection to ensure good load regulation. The internal re ence is referred to GND, so errors at this pin are multiplied by the error amplifier.			
4	V <sub>SW</sub> Internal Power Switch Output. The switch output can swing 35V below ground and is rated to			
5	V <sub>IN</sub>	$V_{\rm IN}$ supplies power to the internal circuitry and also connects to the collector of the internal power switch. $V_{\rm IN}$ must be bypassed with a low-ESR capacitor, typically 200µF or 220µF.		

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